

# MAYS0750Z

## Silicon planar type

For surge absorption circuits

### ■ Features

- Small terminal capacitance  $C_t$
- High electrostatic discharge ESD

### ■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit
Total power dissipation *1	$P_T$	150	mW
Junction temperature	$T_j$	150	$^\circ\text{C}$
Storage temperature	$T_{\text{stg}}$	-55 to +150	$^\circ\text{C}$
Electrostatic discharge *2	ESD	$\pm 12$	kV

Note) \*1:  $P_T = 150$  mW achieved with a printed circuit board.

\*2: Test method: IEC61000-4-2

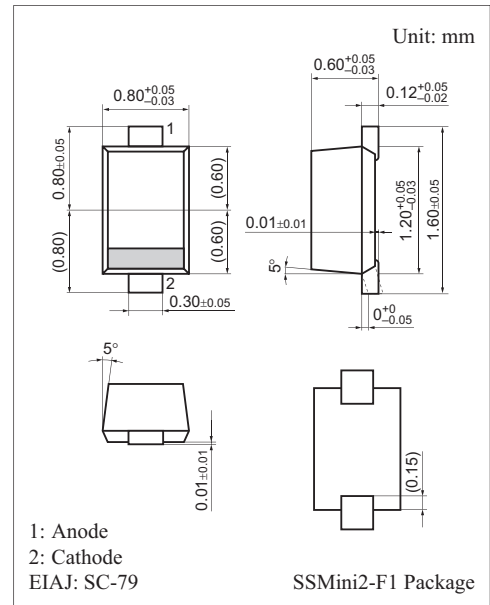
( $C = 150$  pF,  $R = 330 \Omega$ , Contact discharge: 10 times)

### ■ Electrical Characteristics $T_a = 25^\circ\text{C} \pm 3^\circ\text{C}$

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Breakdown voltage *	$V_{\text{BR}}$	$I_R = 1$ mA	6.0	7.5		V
Reverse current	$I_R$	$V_R = 5$ V			2	$\mu\text{A}$
Terminal capacitance	$C_t$	$I_R = 0$ V, $f = 1$ MHz		1.5	3.0	pF

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 measuring methods for diodes.

2. \*:  $V_Z$  guaranteed 20 ms after current flow.



Marking Symbol: CZ

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